

Attorney's Docket No.: 10559-310US1 /INTEL P9631

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 06 2006

Applicant : Gilbert Wolrich et al.

Art Unit : 2183

Assignee : Intel Corporation

Examiner : Daniel H. Pan

Serial No. : 10/070,091

Filed : February 27, 2002

Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR  
ARCHITECTURE

## MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO FINAL ACTION OF FEBRUARY 7, 2006

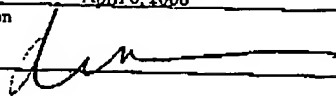
Please amend the above-identified application as follows:

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